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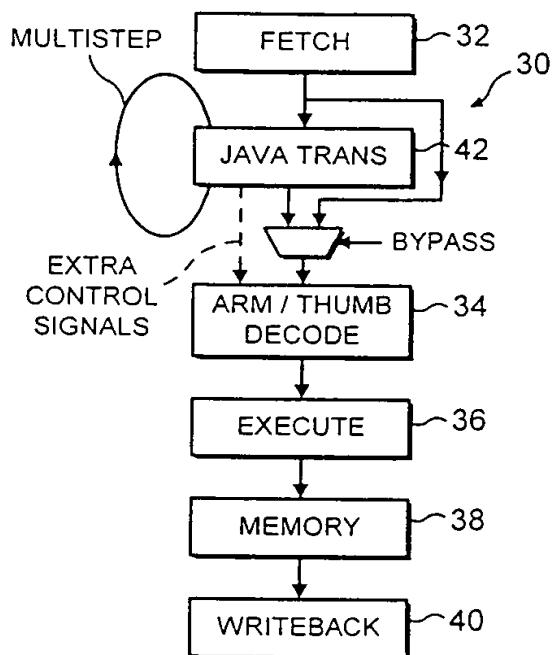


FIG. 1

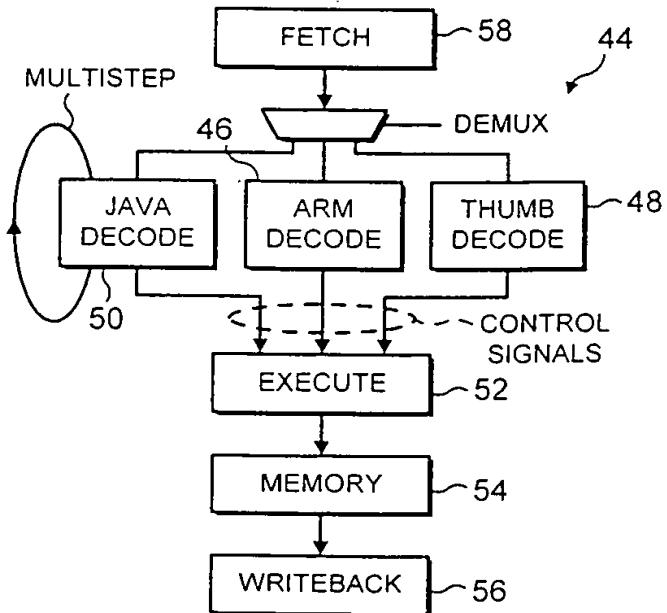


FIG. 2

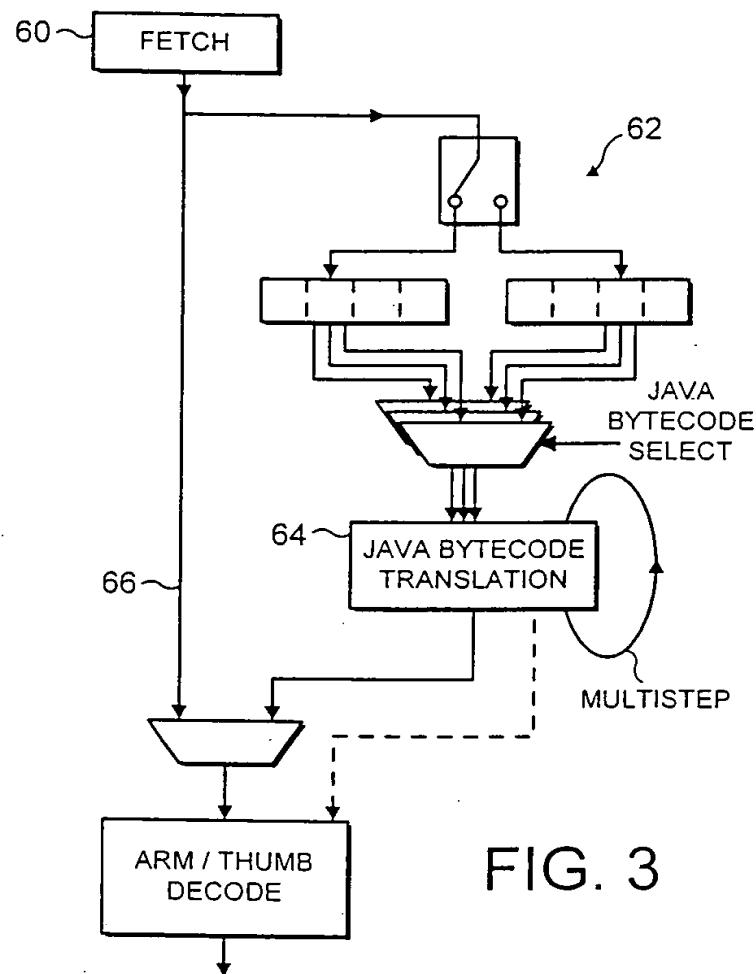
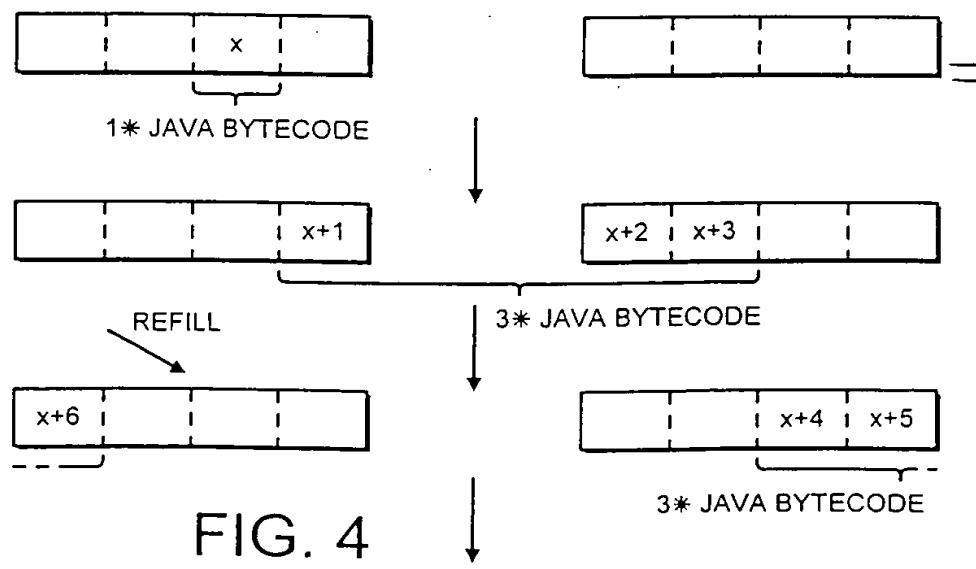


FIG. 3



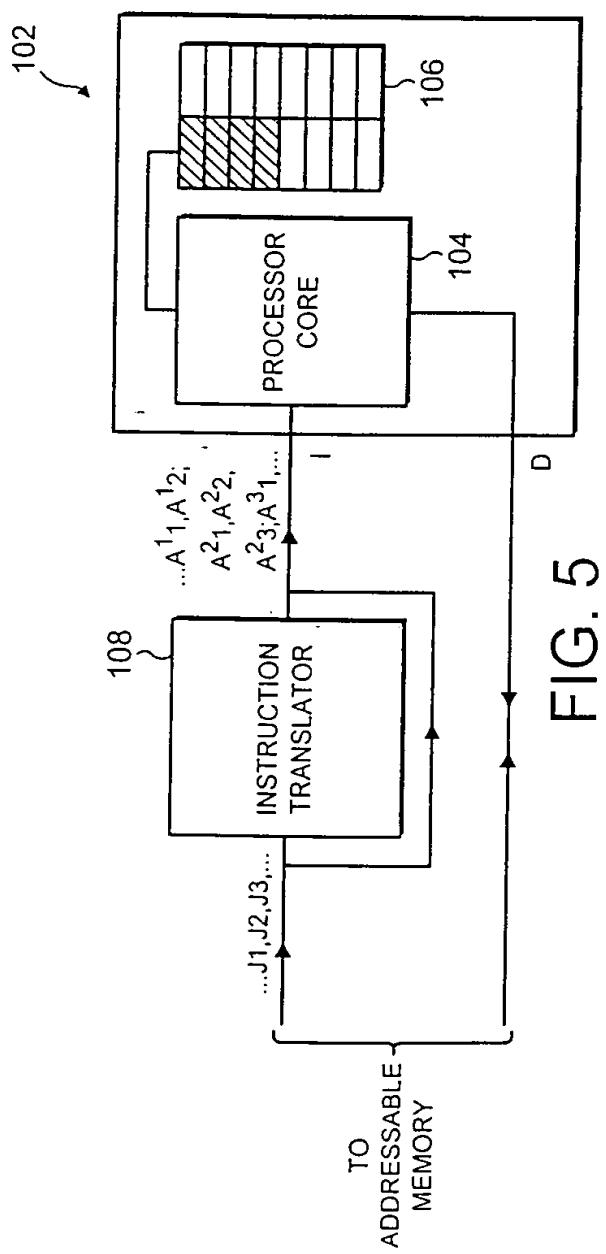


FIG. 5

ARM INSTRUCTION	Java Instruction	ARM INSTRUCTION(S)	Java Instruction
	iadd (RF=2,RF> \emptyset)		iadd (RF=2,RF>1)
	✓ LDR R0[RStack,#-4]! (POP)	LDR R3[RStack,#-4]! (POP)	(SA=-1)
STATE	00000	00100	01000
R0	E	SOA TOS	SOA TOS
R1	E	E	E
R2	E	E	E
R3	E	E	SOB TOS-1 (SOA+SOB) TOS

ARM INSTRUCTION	Java Instruction	ARM INSTRUCTIONS	Java Instruction
	load ¹ (RF= \emptyset ,RE>2)		load ¹ (RF= \emptyset ,RE>2)
	✓ LDR R1,[Rvars,#4] LDR R0,[Rvars,#0]!	✓ STR R3[RStack],#-4 (PUSH)	load ² (RF= \emptyset ,RE>2)
STATE	00000	00100	01000
R0	E	SOC TOS-1	SOC TOS-1
R1	E	SOD TOS	SOD TOS
R2	E	E	SOE TOS-1
R3	(SOA+SOB) TOS	(SOA+SOB) TOS	SOF TOS

FIG. 6

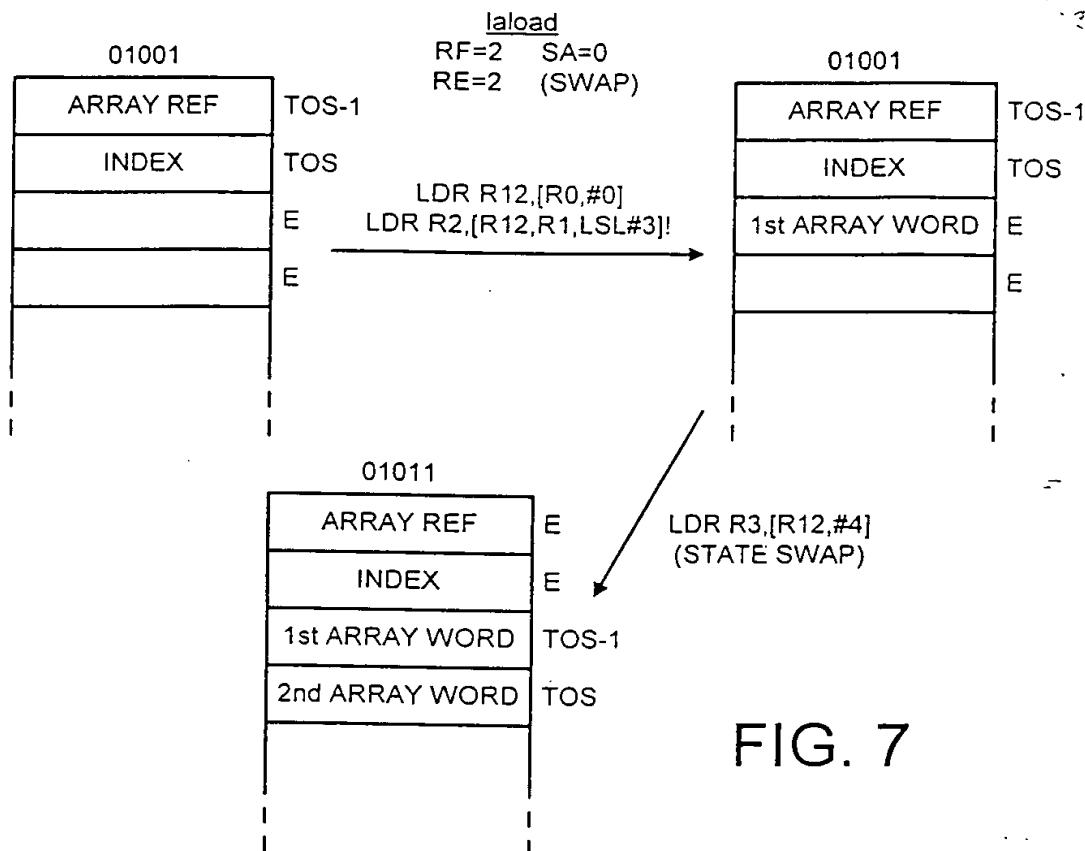


FIG. 7

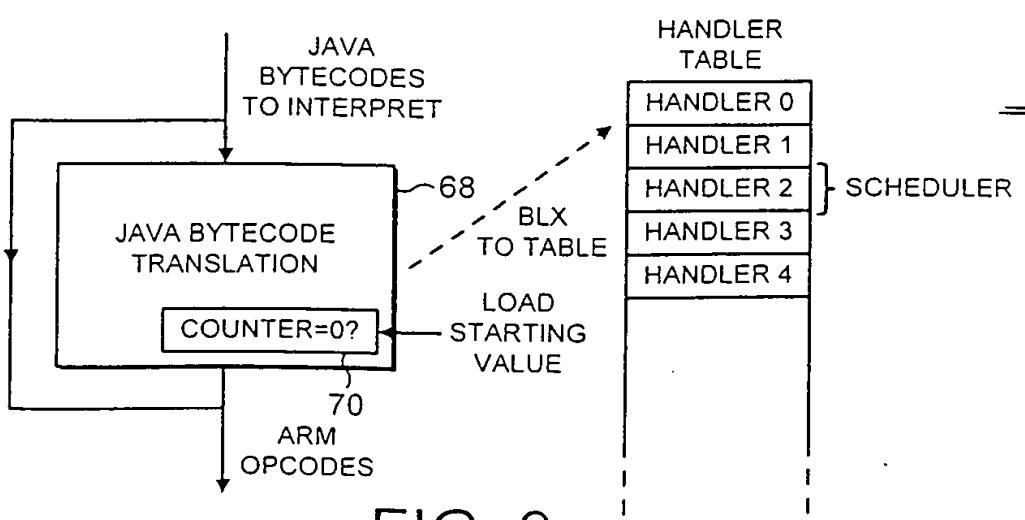


FIG. 9

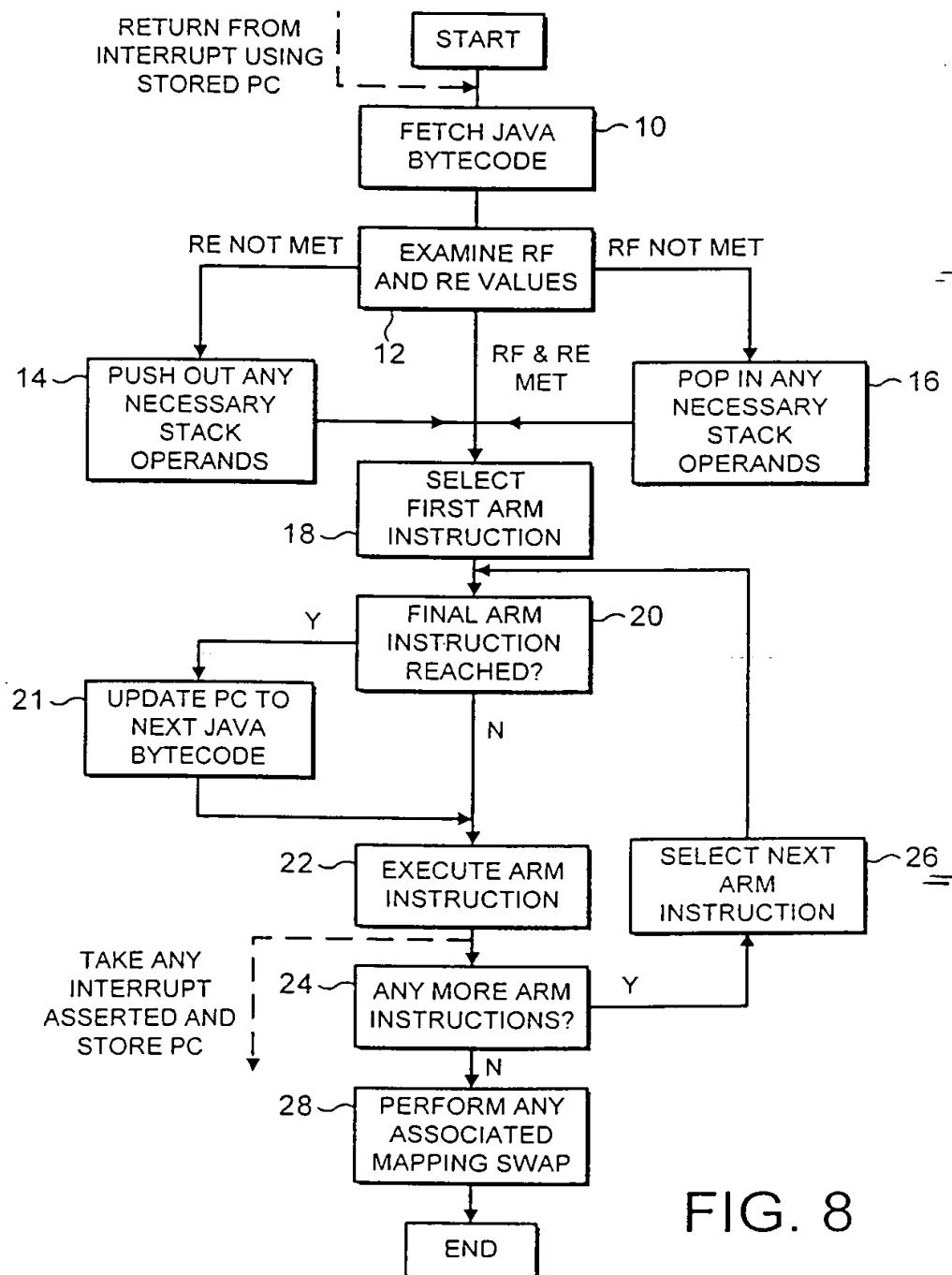
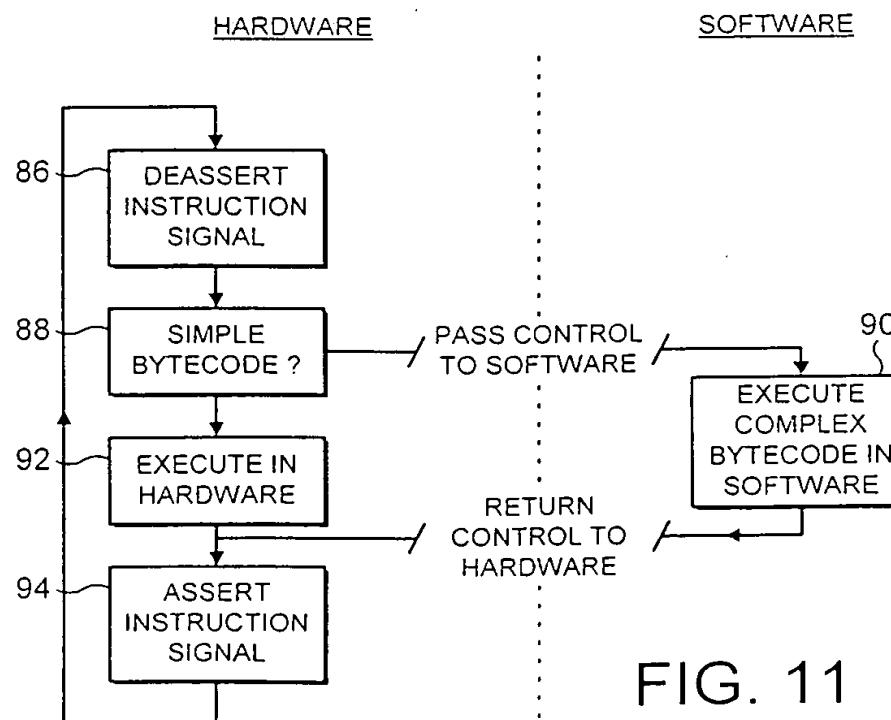
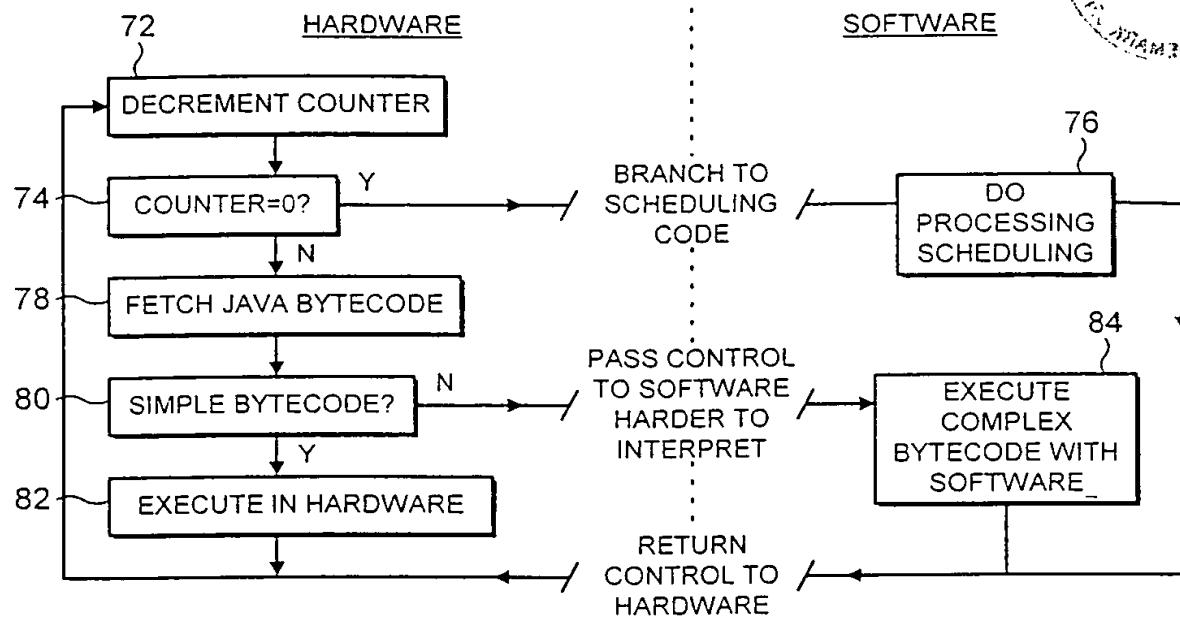


FIG. 8



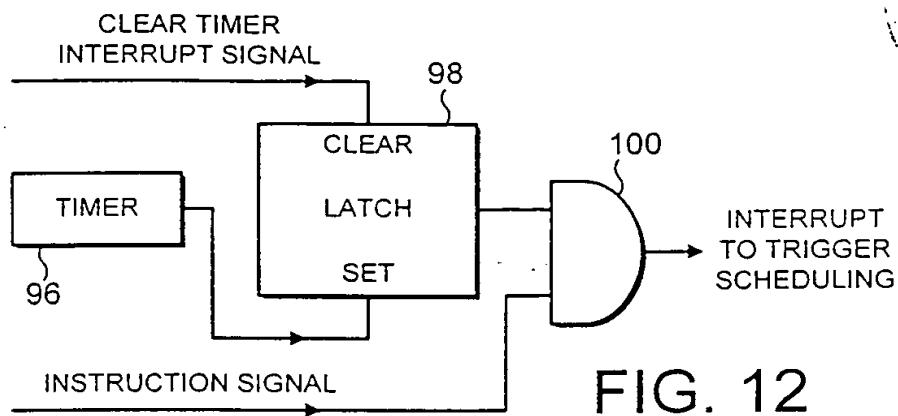


FIG. 12

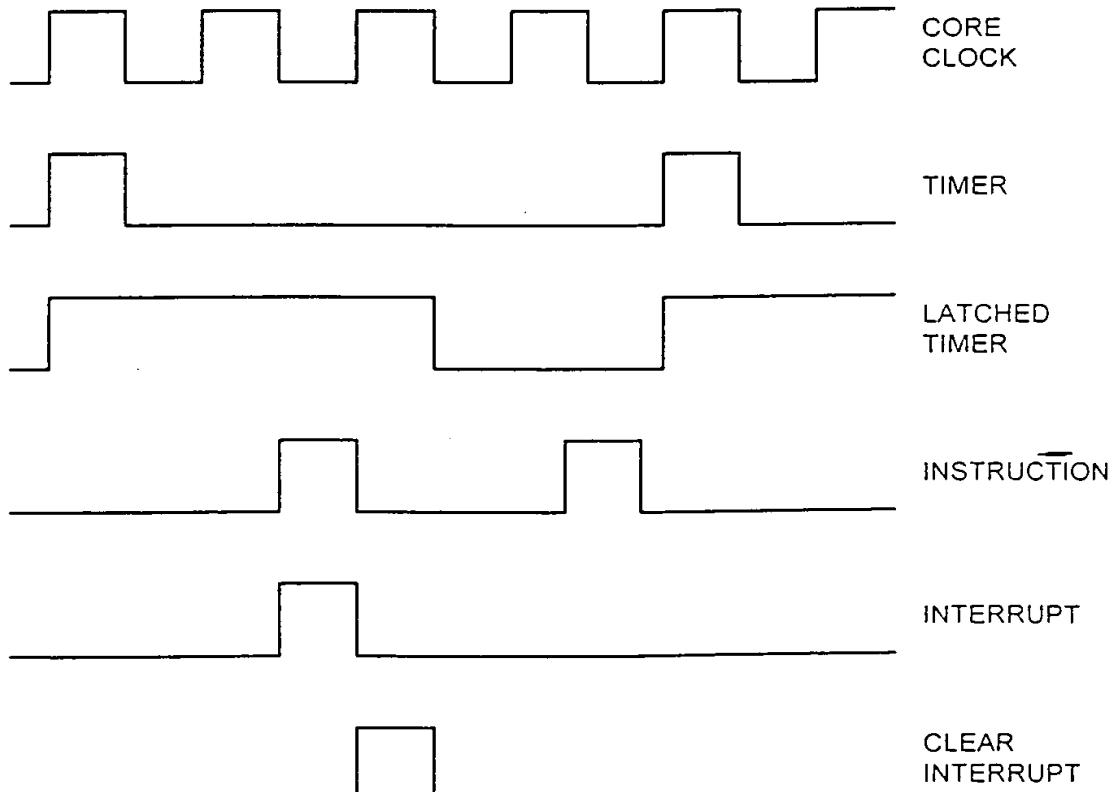


FIG. 13

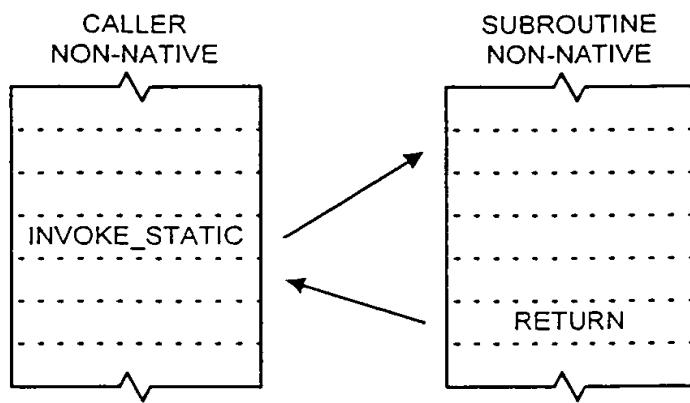


FIG. 14

PRIOR ART

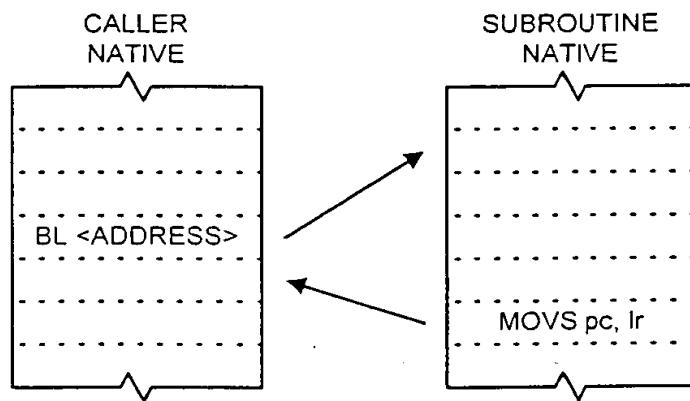


FIG. 15

PRIOR ART

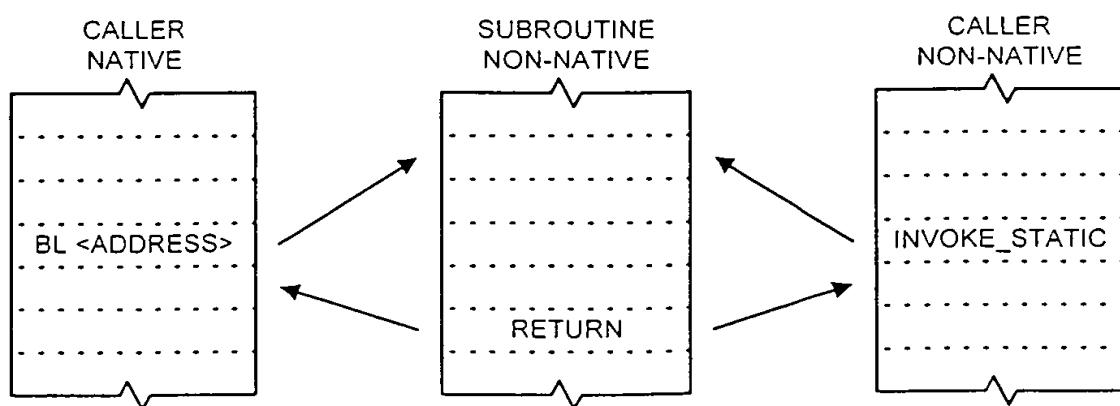


FIG. 16

PRIOR ART

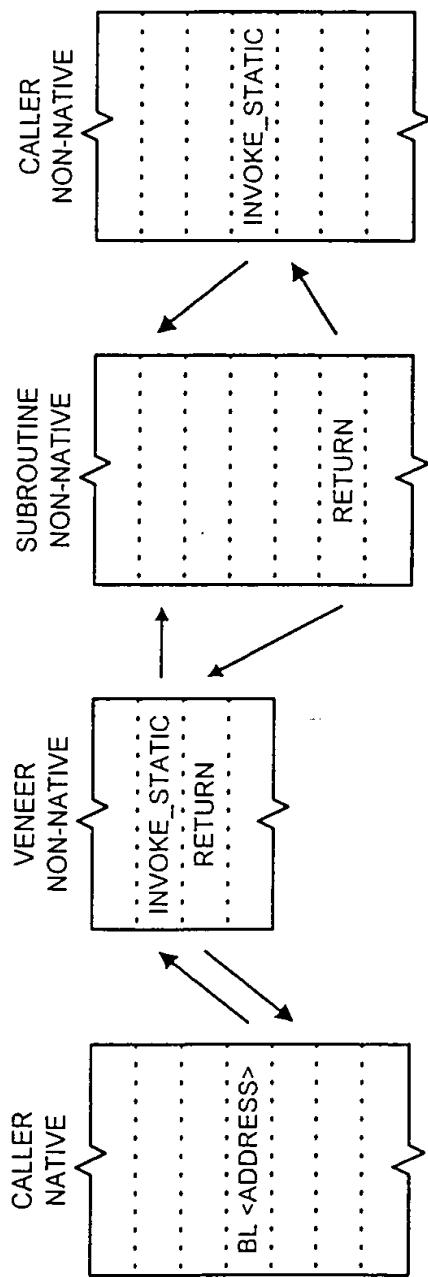


FIG. 17
PRIOR ART

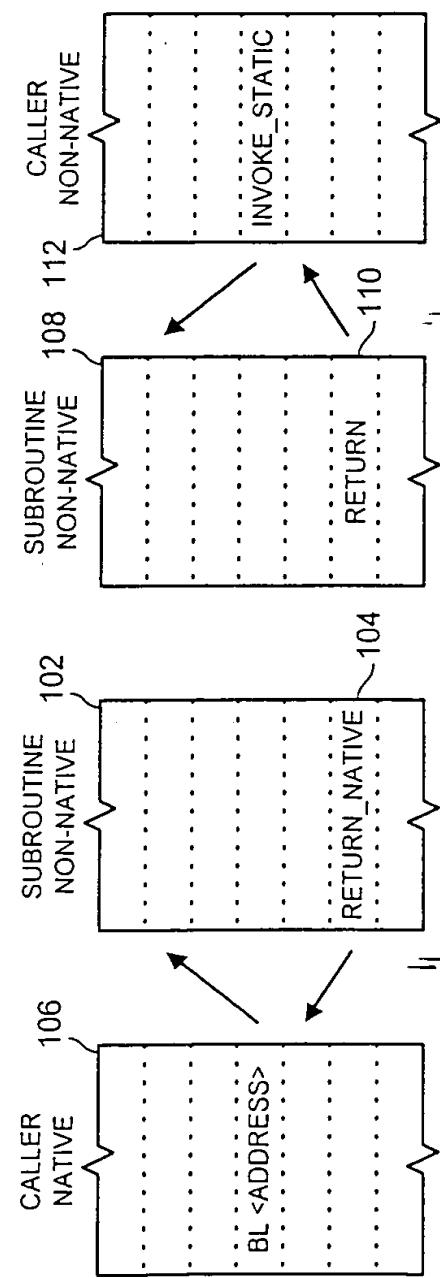


FIG. 18

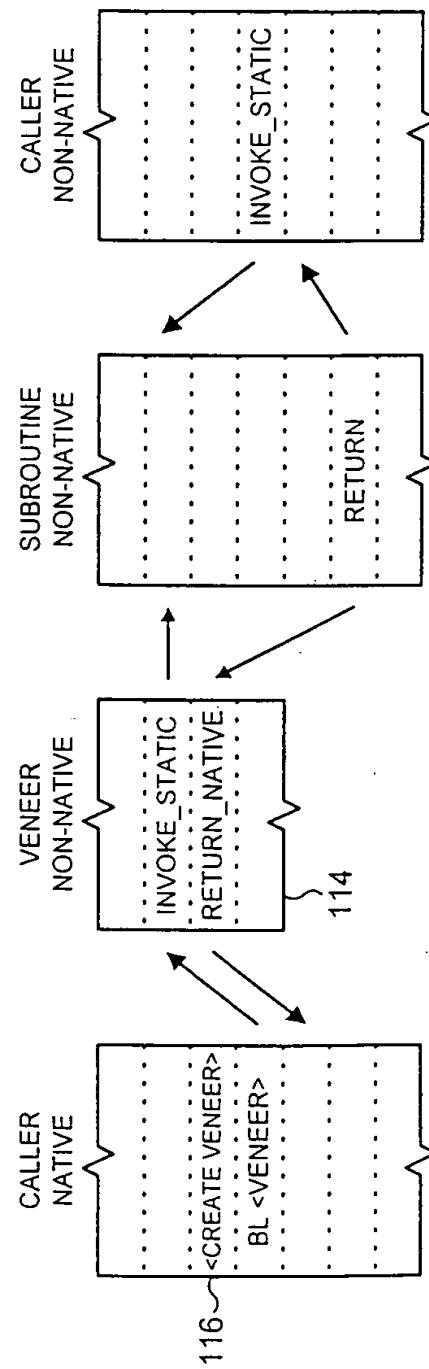


FIG. 19

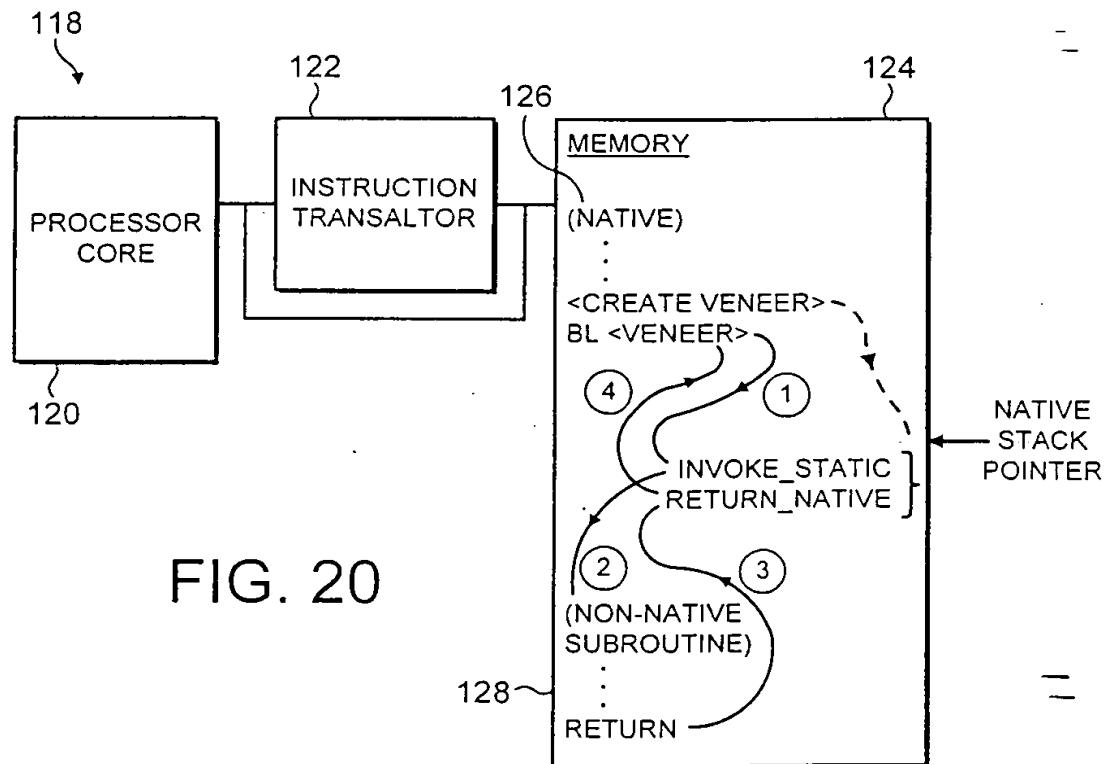


FIG. 20

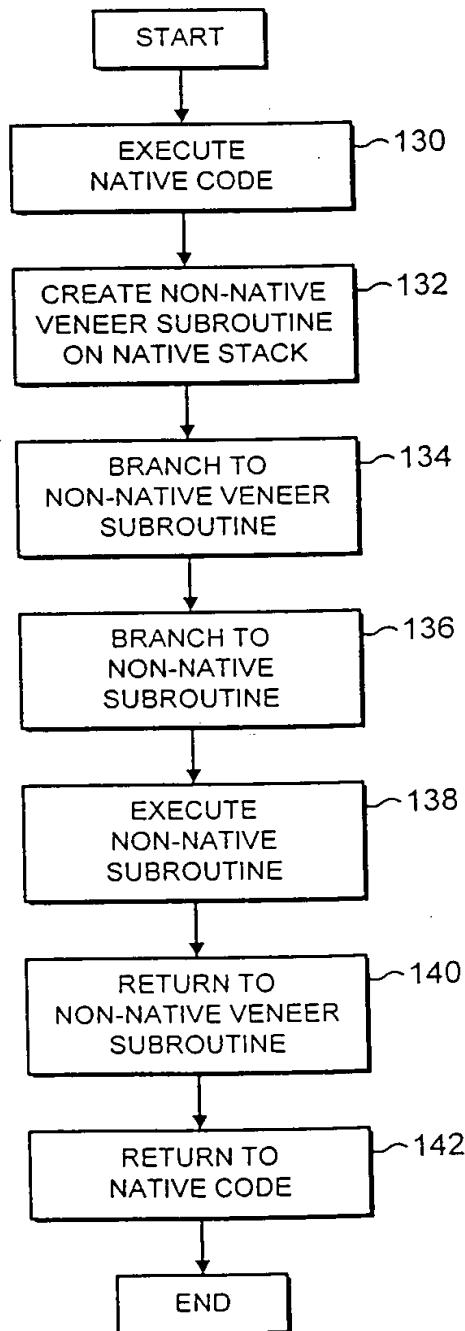


FIG. 21